

Electronic Acknowledgement Receipt

EFS ID:	8865974
Application Number:	10595303
International Application Number:	
Confirmation Number:	7114
Title of Invention:	Method and Device For Secure, Insulated and Electrically Conductive Assembling Of Treated Semiconductor Wafers
First Named Inventor/Applicant Name:	Roy Knechtel
Customer Number:	21967
Filer:	Yisun Song/Robin Taylor
Filer Authorized By:	Yisun Song
Attorney Docket Number:	60291.000048
Receipt Date:	18-NOV-2010
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Application Type:	U.S. National Stage under 35 USC 371

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Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$ 180
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		IDS.pdf	211440	yes	5
			12c4f4c3a8e2b8738e28faf7ec084c81d9f608b7		

	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Information Disclosure Statement (IDS) Filed (SB/08)		1	3	
	NPL Documents		4	5	

Warnings:

Information:

2	Fee Worksheet (PTO-875)	fee-info.pdf	30303	no	2
			0a0f598cbcac849bb1b68af54b94759426849c33		

Warnings:

Information:

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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